

ABSTRACT

In a plasma display panel including scan electrodes, sustain electrodes, and address electrodes, first pulse voltage for the address electrodes or second pulse voltage for the address electrodes is applied to the address electrodes, in an aging step in which aging discharge is performed by alternately applying pulse voltage for the scan electrodes and pulse voltage for the sustain electrodes at least across the scan electrodes and the sustain electrodes. The first pulse voltage has rising edge timing synchronizing with rising edge timing of the pulse voltage for the scan electrodes and a pulse width smaller than that of the pulse voltage for the scan electrodes. The second pulse voltage has rising edge timing synchronizing with rising edge timing of the pulse voltage for the sustain electrodes and a pulse width smaller than that of the pulse voltage for the sustain electrodes.